



Inventor: Yasutaka SAKAINO et al.
Title of Invention: Semiconductor Integrated
Circuit
Attorney Docket No.: 32014-141666
VENABLE

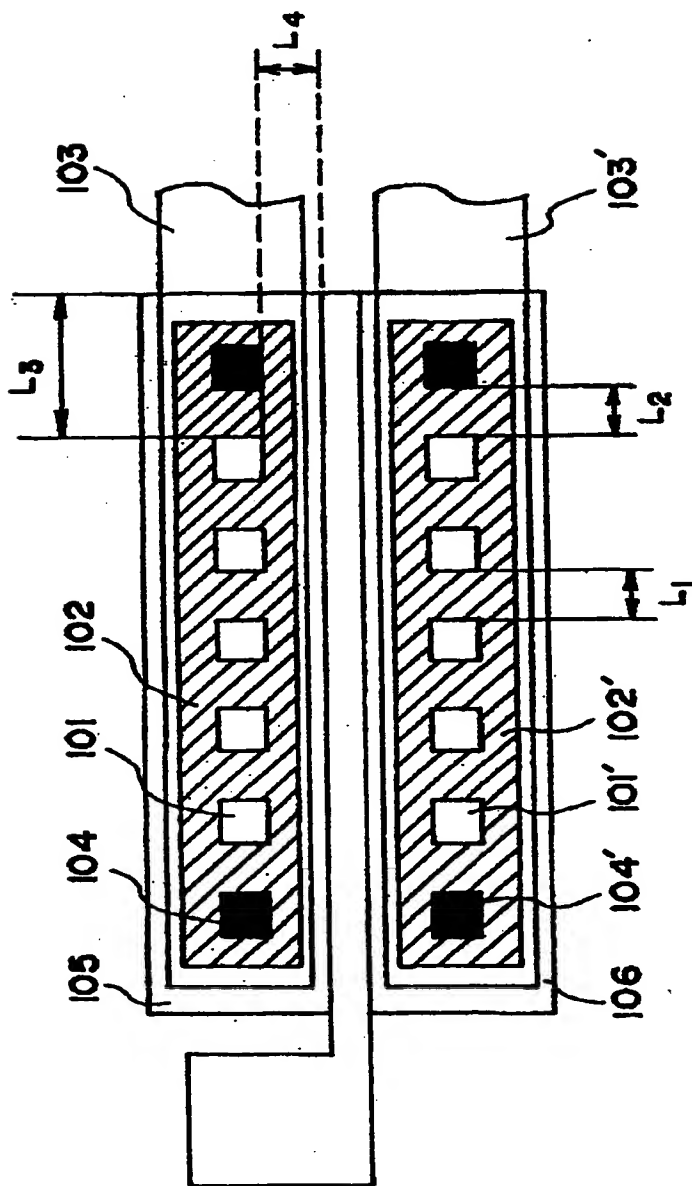


FIG. 1



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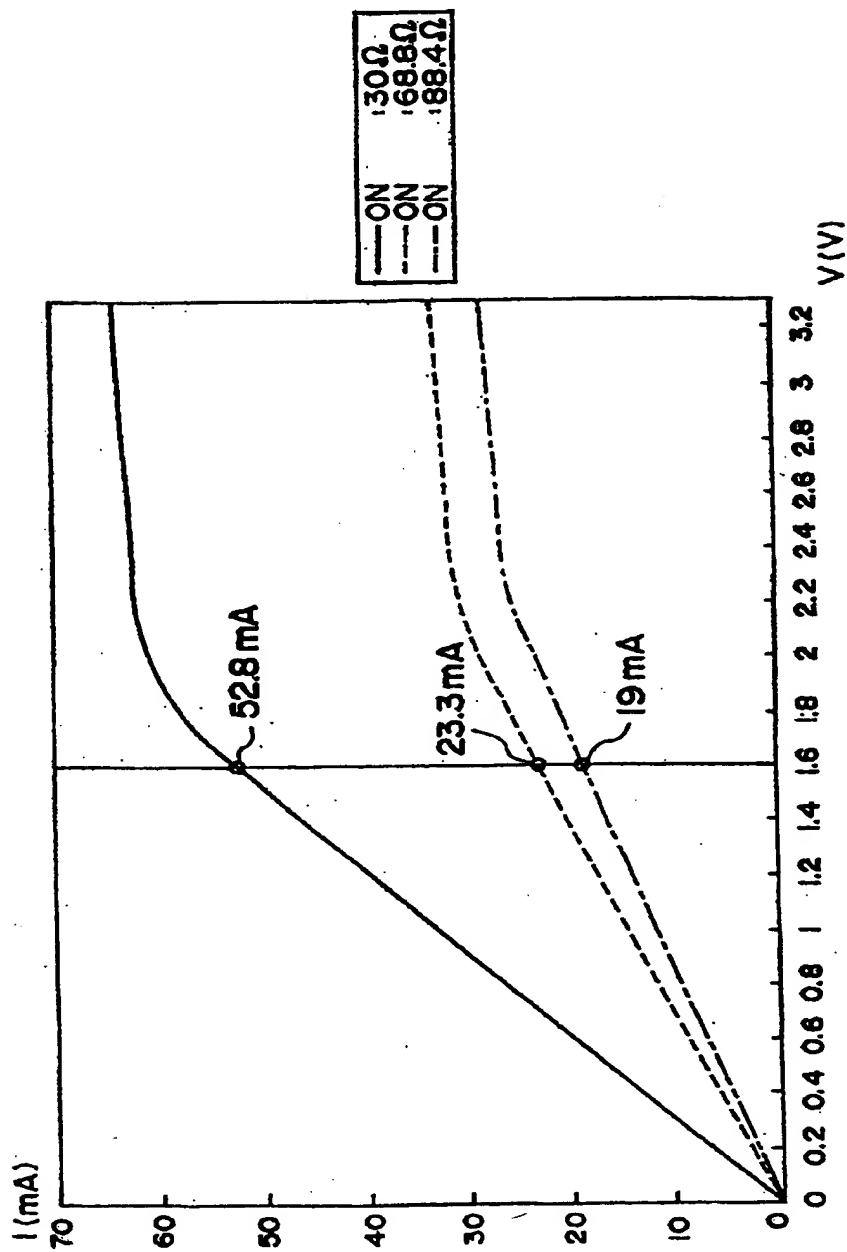
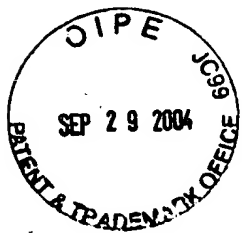


FIG. 2



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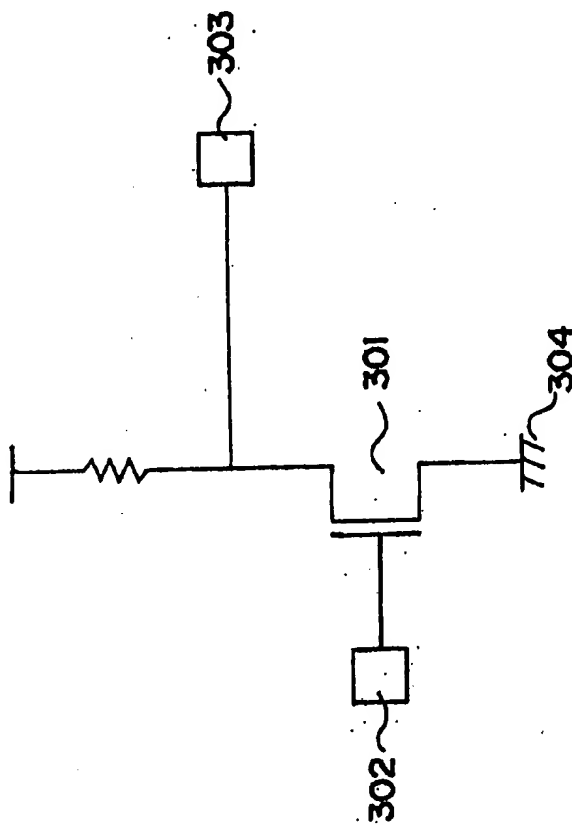
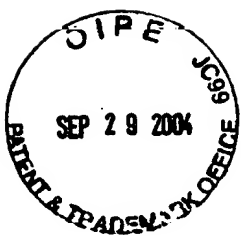


FIG. 3
PRIOR ART



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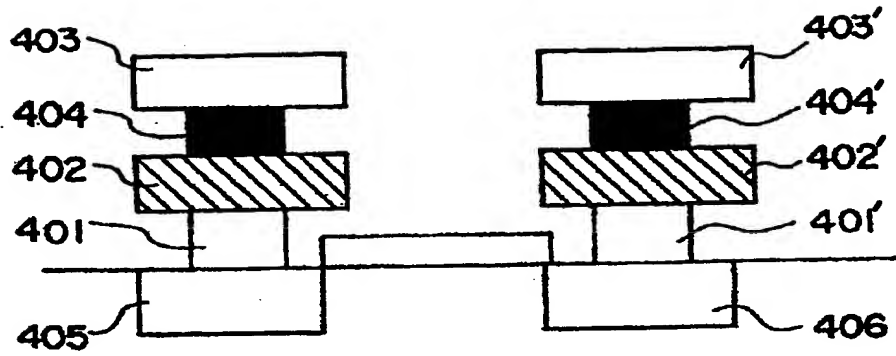


FIG. 4A
PRIOR ART

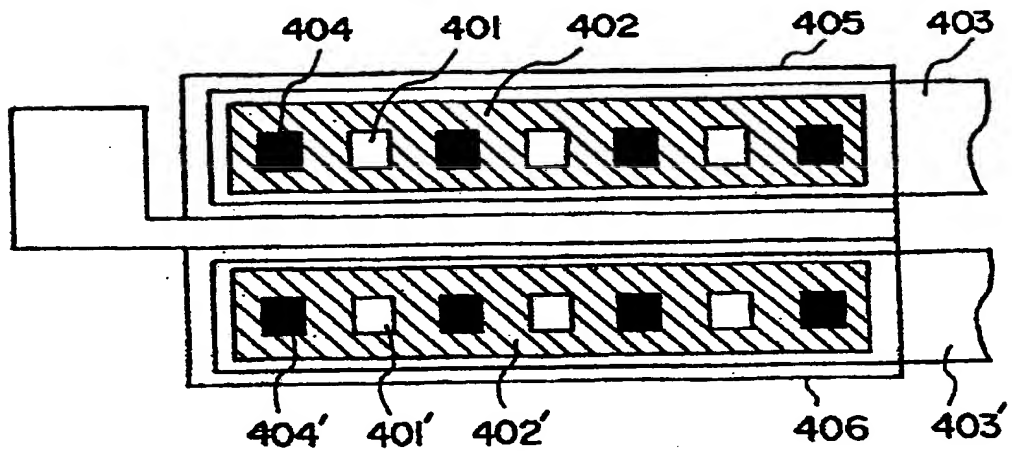
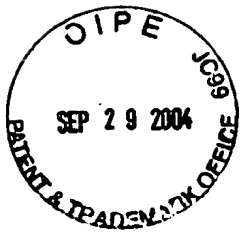


FIG. 4B
PRIOR ART



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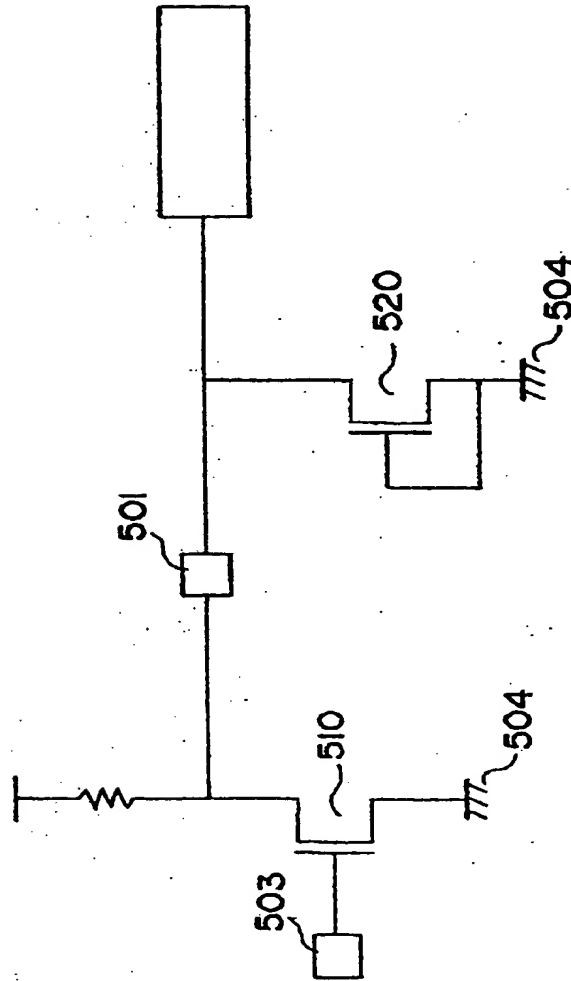


FIG. 5



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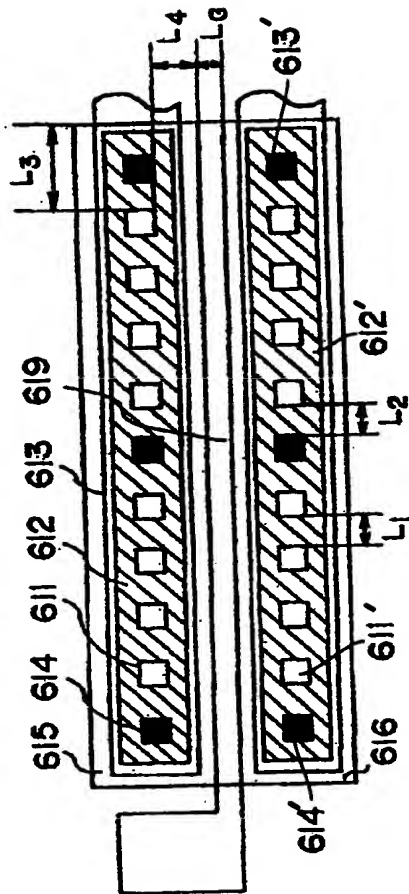


FIG. 6A

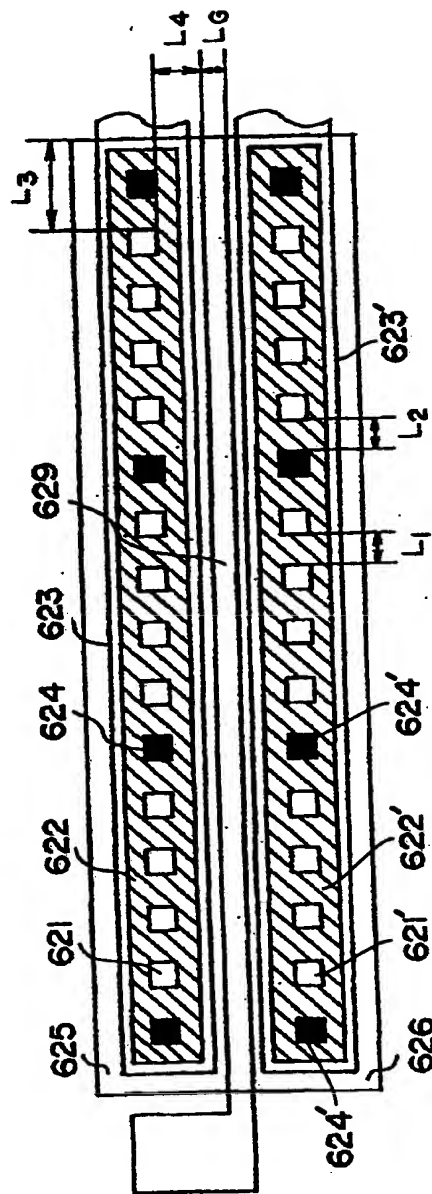


FIG. 6B